

## Geoff Weil

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**Objective:** Individual contributor or project leader position  
in new product development or R&D.

### Experience:

- 8/00 - 5/02** Principal Analog Development Engineer  
Ion Systems, Berkely CA
- 9/99 - 3/00** Engineer  
Comtest BV, Zoeterwoude, Netherlands
- 7/98 - 6/99** Consultant  
EMC Master BV, Naarden, Netherlands
- 4/97 - 5/98** Director, Advanced Technology Group  
Datacom Technologies Inc. Everett WA
- 5/86 - 2/97** Director of Research and Development  
KeyTek Instrument,  
Sr. Engineer, 86 - 89  
Staff Scientist 89 - 92  
Director, R&D 92 - 97
- 3/84 - 2/86** Engineer  
Gamewell Corporation  
Medway, MA
- 6/82 - 10/83** Engineer  
Hyperion Industries Corporation  
Watertown, MA
- 5/81 - 4-82** Research Staff Member, Physics Department  
Massachusetts Institute of Technology Cambridge, MA

### Accomplishments:

- ïProject leader for major product development projects leading to important improvements in the state of the art in EMC simulation.
- ïImportant innovations in ESD simulation leading to patents and unique products.
- ïResponsible for development and introduction of ESD simulators that have become industry standards.

### Areas of Special Expertise:

- ïHigh voltage, broadband/fast risetime pulse techniques, analog, RF, EMC, ESD.

**Publications:**

Making Accurate and Repeatable Measurements of the ESD Current Waveform at 1 Ghz and Above  
7<sup>th</sup> International Zurich Symposium and Technical Exhibition on Electromagnetic Compatibility.  
Zurich, Switzerland, 1987

Characterization and Test Methods for Printed Circuit Board ESD  
IEEE International Symposium on EMC  
Washington DC, 1990

Characterizing the Stress Applied to Ics by Different ESD Tester Circuits  
(with Boxleitner and Richman)  
IEEE International Symposium on EMC  
Washington, DC 1990

Survey of Furniture ESD  
9th International Zurich Symposium and Technical Exhibition on Electromagnetic Compatibility.  
Zurich, Switzerland, 1981

ESD Simulator Tip Voltage at the Moment of Discharge  
(with Boxleitner and Richman)  
IEEE International Symposium on EMC, 1991

Electrical Inrush Transients  
Euro-EMC  
Sandown, UK, 1994

Current Inrush Transients From Application of AC Mains Power  
11<sup>th</sup> International Zurich Symposium and Technical Exhibition on Electromagnetic Compatibility.  
Zurich, Switzerland, 1995

Mains Voltage Dips and Short Interruptions  
EMC Kompendium  
Berlin, Germany, 1997

Stray Line Frequency Fields as a Charging Source  
Geoff Weil, Al Wallash and Larry Levit  
Head Media  
Las Vegas, 2001

Reticle Boxes, ESD Control and Electrostatic Shielding  
Geoff Weil and Larry Levit  
Sematech, 2001

**Patents:**

US Patent# 4,721,899  
Combined Air-Direct Testing Simulator

Swiss Patent# CH 675637 A5  
Gleichstrom-Luftstreckenentladungs-Testsimulator

US Patent # 4,926,285  
Electrostatic Discharge Simulator with Highly Reproducible Wavefront

System and Method for Monitoring and Controlling Electrostatic Charge